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Fig. 1

FIG. 2 is a block diagram of a network switch 200. The switch 200 includes a plurality of input ports 201, 203, 205, 207, 209, 211, and 213. The input ports 201, 203, 205, 207, and 209 are connected to a shared class queue 207. The input ports 211 and 213 are connected to a shared best effort queue 209. The shared class queue 207 and the shared best effort queue 209 are connected to an arbiter 211. The arbiter 211 is connected to an output port 215.

200

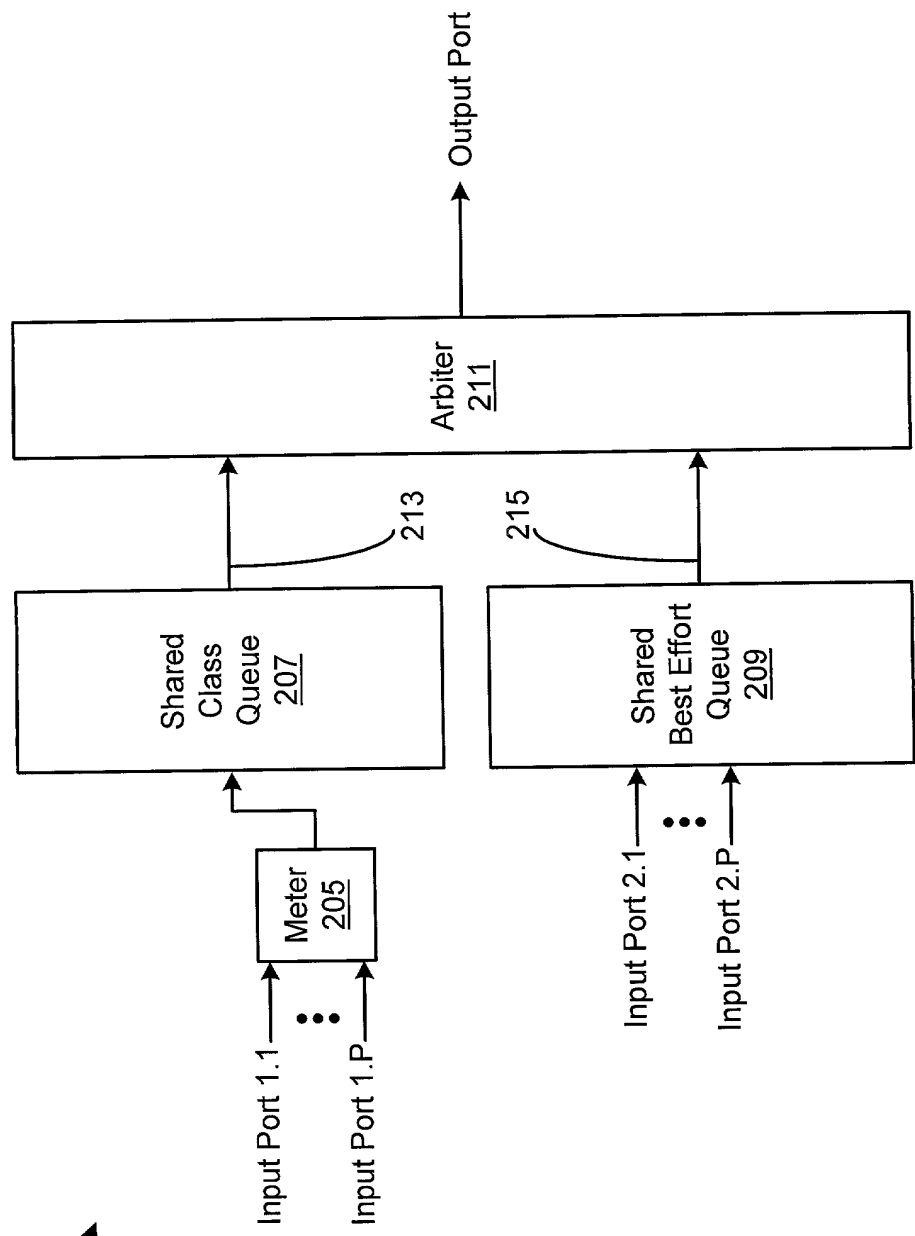
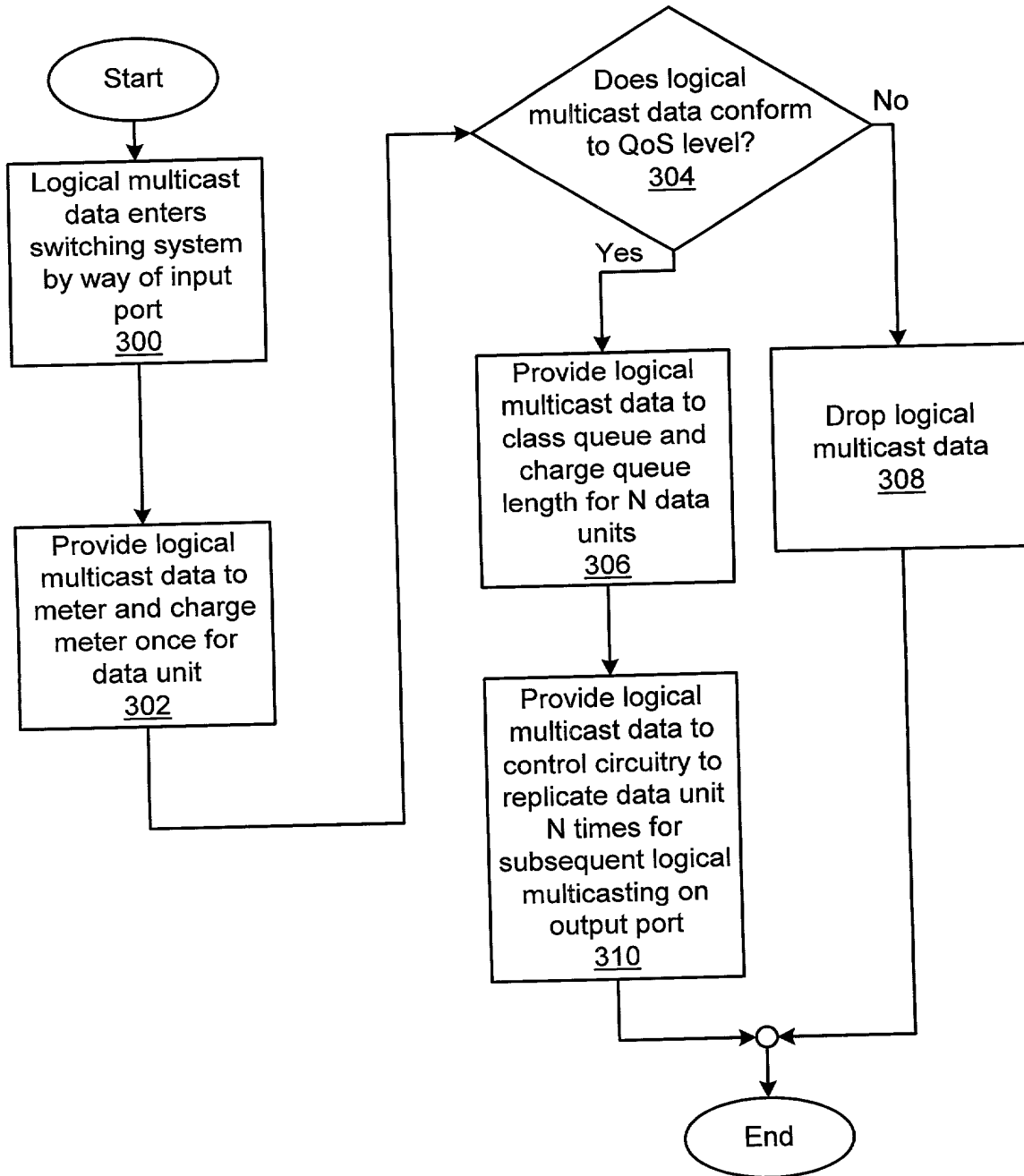


Fig. 2

**Fig. 3**